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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/851,348	05/09/2001	Masahiro Naito	1190-0498P	3044

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BIRCH STEWART KOLASCH & BIRCH  
PO BOX 747  
FALLS CHURCH, VA 22040-0747

EXAMINER

NGUYEN, KIMNHUNG T

ART UNIT PAPER NUMBER

2674

DATE MAILED: 07/03/2003

7

Please find below and/or attached an Office communication concerning this application or proceeding.

# Office Action Summary

Application No.

09/851,348

Applicant(s)

NAITO ET AL.

Examiner

Kimnhung Nguyen

Art Unit

2674

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

## Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

## Status

- 1) ☒ Responsive to communication(s) filed on 09 March 2003.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

## Disposition of Claims

- 4) ☒ Claim(s) 1-10 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-10 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

## Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on \_\_\_\_\_ is: a) ☐ approved b) ☐ disapproved by the Examiner.
- If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

## Priority under 35 U.S.C. §§ 119 and 120

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
  - ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
- a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

## Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892) 4) ☐ Interview Summary (PTO-413) Paper No(s). \_\_\_\_\_
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948) 5) ☐ Notice of Informal Patent Application (PTO-152)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s) \_\_\_\_\_ 6) ☐ Other: \_\_\_\_\_

### DETAILED ACTION

This application has been examined. The claims 1-10 are pending. The examination results are as following.

#### *Drawings*

1. Figures 9-11 should be designated by a legend such as --Prior Art-- because only that which is old is illustrated. See MPEP § 608.02(g). A proposed drawing correction or corrected drawings are required in reply to the Office action to avoid abandonment of the application. The objection to the drawings will not be held in abeyance.

#### *Claim Rejections - 35 USC § 103*

2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

3. Claim 1 is rejected under 35 U.S.C. 103(a) as being unpatentable over figures 9-11 (admitted by Applicant) in view of Nobutani et al. (US patent 5,736,981) and in view of Hanami et al. (US patent 6,125,432).

Prior Art in figure 9-11 disclose a display control device (7) including an image data writing means (1), a graphics memory (2) connected to the writing means, a data transfer means (3) responsive to a command from the writing means for reading data from the graphic memory, and transferring data to a display means (4). However, Prior Art of figures 9-11 does not disclose a write region detection means responsive to addresses

Art Unit: 2674

accessed by the image data writing means for detecting a region including all the addresses. Nobutani et al. disclose a write Hanami et al. disclose in figure 4A, a write region detection means (5, see rewrite detector 5, see column 11, lines 14-22). Hanami et al. disclose the addresses accessed by the image data writing (see image data transfer region, see column 8, lines 24-35, and see column 8, lines 55-66). It would have been obvious to one of ordinary skill in the art at the time the invention was made to utilize the teachings of Nobutani et al. including a write region and Hanami et al. with data transfer region as discussed above into the display system of Prior Art for producing the claimed invention because this would detect an address for accessing the display data memory and to cause the display controller to perform the partial rewrite operation (see Nobutani et al. of abstract), and for the data transfer to write operation and vice versa in order to carry out the precharging operation and the write operation concurrently, so that the pixel data can be transferred at a high data transfer rate and field data can be transferred independently (see Hanami et al. of abstract).

4. Claims 2-6 are rejected under 35 U.S.C. 103(a) as being unpatentable over Prior art of figures 9-11 in view of Nobutani et al. (US patent 5,736,981) and in view of Hanami et al. (6,125,432) as applied to claim 1 above, and further in view of Shimizu (US patent 6,043,803).

Prior Art of figures 9-11, Nobutani et al. and Hanami et al. teach a display controller including an image data writing means (1), a graphics memory (2) connected to the writing means, a data transfer means (3) responsive to a command from the writing means for reading data from the graphic memory, and transferring data to a display means, and a write region detection means, and the addresses accessed by the image data

Art Unit: 2674

writing as discussed above. Hanami et al. further more disclose the region from the vertical and horizontal direction address accessed by the writing means and rectangular region (see column 7, lines 53-64), and data writing means for detecting the region including all the addresses (see row address and column address, column 8, lines 24-34). However, Prior Art, Nobutani et al. and Hanami et al. do not disclose the minimum vertical direction address to the maximum vertical direction address among the address accessed by said image writing means, and from the minimum horizontal direction address to the maximum horizontal direction address among the address accessed by said image writings. Shimizu discloses a minimum start position detecting circuit and maximum end position detecting circuit in a vertical sync and horizontal sync, that is the minimum horizontal direction address to the maximum horizontal direction address. It would have been obvious to one of ordinary skill in the art at the time the invention was made to utilize the teachings of Shimizu a minimum start position detecting circuit and maximum end position detecting circuit in a vertical sync and horizontal sync as taught by Shimizu into the display system of Prior Art, Nobutani et al. and Hanami et al. for producing the claimed invention because this would be indicated a full size for screen from a horizontal sync and vertical sync signal.

5. Claims 7-10 are rejected under 35 U.S.C. 103(a) as being unpatentable over Prior art of figures 9-11 cited by applicant in view of Hanami et al. (US patent 6,125,432) and in view of Shimizu (US patent 6,043,803).

Prior art of figures 9-11 disclose a machine-readable medium (liquid crystal driver circuit 6 reads the data from memory 5) having stored thereon a plurality of executable, the

Art Unit: 2674

plurality of instructions comprising to access image data (1) and a memory (5) for transfer to the display screen (7). However, Prior Art of figures 9-11 do not disclose an image data region less than a full display screen of image data, a transfer image within the image data region and a region having rectangular region from a minimum vertical direction address to a maximum vertical direction address among the address being accessed. Hanami et al. disclose an image data region to addresses accessed by the image data writing (see image data transfer region, see column 8, lines 24-35, and column 8, lines 55-66) and an inherent less than a full display screen of image data and a region having rectangular region from a vertical direction address to a vertical direction address among the address being accessed (see column 7, lines 53-64). Shimizu discloses a minimum start position detecting circuit and maximum end position detecting circuit in a vertical sync and horizontal sync, that is the minimum horizontal direction address to the maximum horizontal direction address. It would have been obvious to one of ordinary skill in the art at the time the invention was made to utilize the teachings of data region to addresses accessed by the image data writing as taught by Hanami et al. and Shimizu with a minimum start position detecting circuit and maximum end position detecting circuit in a vertical sync and horizontal sync as taught by Shimizu into the display system of Prior Art and Hanami et al. for producing the claimed invention because this would be make a write operation and precharging operation can be concurrent carried out and hence the data transfer time can be reduce (see column 10, lines 29-31) and for minimum start position detecting circuit and maximum end position detecting circuit in a vertical

Art Unit: 2674

sync and horizontal sync to indicate a full size for screen from a horizontal sync and vertical sync signal.

***Response To Arguments***

6. Applicant's argument filed on 3-19-03 has been fully considered but they are not persuasive in view of new ground rejection.

Applicant argues that neither Kuwajima nor Tasai disclose a write detection means for detecting a region including all the addresses by the image in the writing. However, this argument is not persuasive due to the teaching of new Prior Art of the combination of Nobutani et al. (5,736,981) and Hanami et al. (US patent 6,125,432) as disclosed above.

***Correspondence***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kimnhung Nguyen whose telephone number (703) 308-0425.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, **RICHARD A HJERPE** can be reached on **(703) 305-4709**.

**Any response to this action should be mailed to:**

Commissioner of Patents and Trademarks

Washington, D. C. 20231

**Or faxed to:**

Art Unit: 2674

**(703) 872-9314 (for Technology Center 2600 only).**

Hand-delivery response should be brought to: Crystal Park II, 2121 Crystal Drive,  
Arlington, VA Sixth Floor (Receptionist).

Any inquiry of a general nature or relating to the status of this application or proceeding  
should be directed to the Technology Center 2600 Customer Service Office whose telephone  
number is (703) 306-0377.

Kimnhung Nguyen  
June 24, 2003



RICHARD HJERPE  
SUPERVISORY EXAMINER  
TECHNOLOGY CENTER 2600